

**RECEIVED**  
**CENTRAL FAX CENTER****MAR 21 2006****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

App. No.:	10/501,431	Atty. Docket No.	02PCTEZ1
Filing Date:	07/13/2004	Confirm. No.	9438
Applicants:	S. A. Garyainov, et al.	Examiner	Walter Lee Lindsay, Jr.
Title:	Fluxless Assembly of Chip Size Semiconductor Packages	Art Unit	2812

**Via Facsimile to (571) 273-8300**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Los Angeles, California  
March 21, 2006

**RESPONSE TO FINAL OFFICE ACTION DATED 02/09/2006**

Dear Sir:

In response to the final Office action of 02/09/2006, please amend the above-identified application as follows.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 8 of this paper.